

Amendments to the Claims

Please amend claims 1 and 5 as shown in the following list of claims. This listing of claims will replace all prior versions, and listings, of claims in the application.

1. (currently amended) A data carrier, which is designed to modulate a carrier signal that can be received in a contactless manner, and which is equipped with transmission means, designed to transmit the carrier signal, and which is equipped with an electrical circuit, which circuit is equipped with at least one terminal, to which terminal the transmission means is connected and via which terminal the carrier signal can be fed to the circuit, and which circuit is equipped with a data signal source designed to generate and emit a data signal having only two voltage values, and which circuit is equipped with modulation means designed to receive the data signal and, using the data signal, to modulate the carrier signal occurring at the at least one terminal, and to generate an amplitude-modulated signal having only two amplitudes, in which amplitude-modulated signal edges occur, characterized in that signal-edge influencing means provided, which is designed to influence the slope characteristic of the signal edges in the amplitude-modulated signal.

2. (previously presented) A data carrier as claimed in claim 1, characterized in that the signal-edge influencing means is realized by filtration means.

3. (previously presented) A data carrier as claimed in claim 2, characterized in that the filtration means is provided between the data signal source and the modulation means and designed to filter the data signal that can be emitted from the data signal source to the modulation means.

4. (previously presented) A data carrier as claimed in claim 2, characterized in that the filtration means is formed by a low-pass filter.

1 5. (currently amended) A circuit for a data carrier which is designed to
2 modulate a carrier signal that can be received in a contactless manner, and which
3 is equipped with transmission means to transmit the carrier signal, which circuit is
4 equipped with at least one terminal, to which terminal the transmission means can
5 be connected, and via which terminal the carrier signal can be fed to the circuit,
6 and which circuit is equipped with a data signal source designed to generate and
7 emit a data signal having only two voltage values, and which circuit is equipped
8 with modulation means designed to receive the data signal and, using the data
9 signal, to modulate the carrier signal occurring at the at least one terminal, and to
10 generate an amplitude-modulated signal having only two amplitudes, in which
11 amplitude-modulated signal edges occur, characterized in that signal-edge
12 influencing means is provided, which is designed to influence the slope
13 characteristic of the signal edges in the amplitude-modulated signal.

1 6. (previously presented) A circuit as claimed in claim 5, characterized in that
2 the signal-edge influencing means is realized by filtration means.

1 7. (previously presented) A circuit as claimed in claim 6, characterized in that
2 the filtration means is provided between the data signal source and the modulation
3 means and designed to filter the data signal that can be emitted from the data
4 signal source to the modulation means.

1 8. (previously presented) A circuit as claimed in claim 6, characterized in that
2 the filtration means is formed by a low-pass filter.

1 9. (previously presented) A circuit as claimed in claim 5, characterized in that
2 the circuit is realized as an integrated circuit.

1 10. (previously presented) A circuit as claimed in claim 5, characterized in that
2 the modulation means includes a transistor with a control terminal, and the signal-
3 edge influencing means includes a resistor connected to the control terminal of the
4 transistor and a capacitor connected to the control terminal of the transistor and
5 ground.

1 11. (previously presented) A data carrier as claimed in claim 1, characterized
2 in that the modulation means includes a transistor with a control terminal, and the
3 signal-edge influencing means includes a resistor connected to the control
4 terminal of the transistor and a capacitor connected to the control terminal of the
5 transistor and ground.